



Shri Vile Parle Kelavani Mandal's

DWARKADAS J. SANGHVI COLLEGE OF ENGINEERING

(Autonomous College Affiliated to the University of Mumbai)

NAAC Accredited with "A" Grade (CGPA : 3.18)



REGULAR EXAMINATION TIMETABLE, JANUARY, 2025

MASTER OF TECHNOLOGY (M. Tech.) All Branches

FIRST YEAR, SEMESTER – I (For 2024 Batch Under Autonomy)

Timings: 10.00 a.m. to 12.00 p.m.

Date: 10th December, 2024

Day & Date	Mechanical Engineering	Electronics & Telecommunication Engineering	Computer Engineering
Tuesday, 21/01/2025	Computer Integrated Manufacturing Systems	Statistical Signal Processing	Algorithm and Complexity
Thursday, 23/01/2025	Quality Engineering	Embedded Systems	Computer Vision
Saturday, 25/01/2025	World Class Manufacturing	Microstrip Antenna Design	Natural Language Processing
Tuesday, 28/01/2025	Cyber Security and Laws		

Important Note: Changes if any, in the timetable, shall be communicated on the College website (djsee.ac.in).

Ms. Alka Shukla
Controller of Examinations

Dr. Hari Vasudevan
Principal